

ABSTRACT OF THE DISCLOSURE

A liquid crystal display device having transistors disposed at the intersections of gate lines and source lines, pixel electrodes connected with the transistors, opposite electrodes opposite to these pixel electrodes, and liquid crystal held between said opposite electrodes and said pixel electrodes is disclosed. The pixel electrodes comprise a first pixel electrode and a second pixel electrode. The second pixel electrode are disposed in a layer above an insulating layer which is itself disposed in a layer above the first pixel electrode. The second pixel electrode has a region that does not overlap with the first pixel electrode. The second pixel electrode are electrically connected with the first pixel electrode.

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